The Advanced Carrier Based Pulse Width Modulation Using Third Injection Harmonic Reference Signal on Neutral Point Clamped Inverter

by I Ketut Wiryajati

The Advanced Carrier Based Pulse Width Modulation Using Third Injection Harmonic Reference Signal on Neutral Point Clamped Inverter

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Abstract—This paper proposes development of a Pulse Width Modulation (PWM) scheme that overcomes the weakness of conventional Carrier-Based PWM (CBPWM) performance on Neural Point Clamp Inverter (NPC). This scheme uses the conventional CBPWM technique to developed Advanced CBPWM. This technique uses triangular carriers in different amplitudes that are arranged in composition, then injects the modulated signal that has been decomposed with the third harmonic. The proposed PWM schema was verified using MATLAB/Simulink software. By applying this scheme, the quality of power, voltage and current are improved, and finally Total Harmonic Distortion (THD) can be reduced.

Keywords— Neural Point Clamp Inverter, CBPWM, NPC, inverter, THD, Advance CBPWM.

I. INTRODUCTION

Multilevel inverter is one of the electronic devices that is very well known in the electric power systems application and in industry. Since multilevel inverter has high performance and produces low harmonics [1-3]. There are several types of multilevel inverters such as Neutral Point Clamped, (NPC) Flying Capacitor Inverters. In particular, the Neutral Point Clamped (NPC) converter is generally used at high voltages industry [4-7].

In conventional inverters, increasing the voltage level will increase the number of diodes, as well as for inverters using capacitors. In addition, the increase of voltage level causes higher cost and increasing of voltage unbalanced problem due to the use more diode or capacitor. Furthermore, the switching losses on the switches will also increase. However, Multilevel NPC inverters have advantages and disadvantages over the other inverters, because it no longer requires balancing on the diode or capacitor, this method only requires a separate dc voltage on each H-Bridge Inverter [8-10].

In this paper, an NPC inverter is used discussed. NPC will apply the proposed new modulation technique. The first development of a multilevel inverter was the NPC Inverter (INPC), this is because the topology has the following advantages:

1. The performance and waveform of the inverter is better especially at the fundamental frequency.

2. The use of capacitors can be charged first before being operated

3. The voltage level is very dependent on the capacitor voltage, namely in terms of voltage balance.

There are several modulation techniques to control the inverters. Such as Selected Harmonic Elimination (SHE) or Optimized Harmonic Stepped Waveform (OHSW), Space Vector PWM (SVPWM), and Carrier Based PWM (CBPWM)[11-13]. Among these modulation techniques, the easiest to apply to multilevel inverters is the CBPWM modulation technique. Although CBPWM technique is very easy to implement, there are several challenges or shortcomings of this technique, namely the quality of the power produced is still decreasing. Several recent studies have explored to improve the quality of this power. conventional PWM, but uses a triangular signal composed of the reference signal that has been assigned a Third Harmonic Injection (THI) which is called a decomposition signal.

To answer this challenge, this paper proposes a new scheme called Advance Carrier Based Pulse Modulation (ACBPWM) with decomposed modulation signal. This scheme does not use a triangular carrier waveform in the same amplitude as in conventional PWM, but uses a triangular signal composed of the reference signal that has been assigned a Third Harmonic Injection (THI) which is called a decomposition signal

II. SINUSOIDAL DECOMPOSITION

A. SPWM (Sinus Pulse Width Modulation)

To generate a sinusoidal PWM signal, a sine wave is provided as the reference signal. The frequency of the sine wave is equal to the frequency of the desired output voltage with respect to the modulation of the carrier signal. The frequency of the carrier signal must be higher than the frequency of the reference signal. When both signals are modulated, it will generate a signal pulse for the switching device for the inverter as shown in Figure (1)
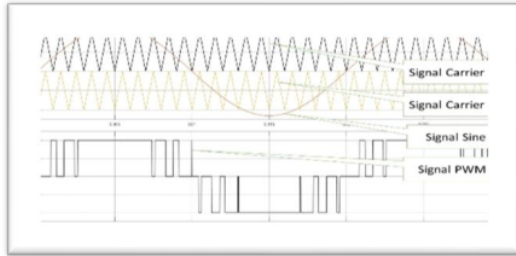
Figure 1 Sinusoidal Pulse Width Modulation (SPWM)

To generate these signals, equation (1) is used, likewise for harmonic signals. This signal will be used as a reference signal or modulation signal in the CBPWM.

$$V_m m\{\sin(\omega_o t) + k.\sin(3.\ \omega_o t)\ \dots\dots\dots\dots\dots\dots\dots(1)$$

With V_m=Amplitude, ω_o=frequency, t=time, m=Modulation index, k= Orde harmonic.

B. Third Harmonic Injection Method

By injecting the third harmonic signal into a sinusoidal signal, we will get a decomposed modulation signal as shown in Figure 2
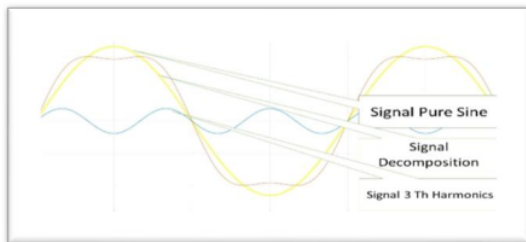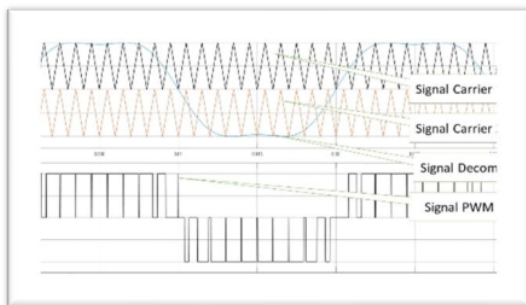

Figure 2 Decomposition with Third Harmonic.

Figure 3 Decomposition signal with two signals

The reference or modulation signal is a third harmonic injection signal which will be modulated with a carrier signal as shown in Figure 3. After being modulated, this signal will generate a signal in the form of a pulse which can then be used as a switching device in the inverter. Signal switching as shown in Figure 3.

III. ADVANCE CARRIER BASED PULSE WIDTH MODULATION

The carrier signal with a triangular shape will serve to get the trigger signal. The proposed method applies a simple logic: If the carrier wave is greater than or equal to the modulating wave the value is high and the switch is turned on and vice versa. Mathematically it can be written as follows:

$$if\ C_r \geq C_m\ then\ On\ \dots\dots\dots\dots\dots\dots\dots\dots\dots\dots \quad (2)$$
$$C_r \leq C_m\ then\ Off$$

The general idea for CBPWM is to use multiple triangular carrier signals, with one carrier signal for each level. For the modulating signal (reference). A carrier signal can be used to signal each phase. If the CBPWM is applied for N phases, reference signal (modulation) corresponds to the number of phases. Figure 4 shows the modified reference signal and carrier signal with a phase shifted while the level is kept constant. The levels of the positive and negative sides are the same so that this CBPWM produces an inverter level 7 CBPWM.
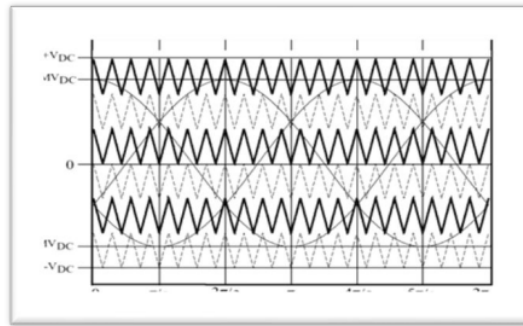

Figure 4. CBPWM 7 level

IV. PROPOSED SCHEME

A. Decomposition of Signals

The scheme proposed in this paper is shown in Figure 5. Each carrier wave in CBPWM has voltage amplitude a peak-to-peak (vpp) equal to that of each source DC voltage. In this figure, the peak-to-peak voltage (vpp) of the carrier wave 3 is -0. 75 Vdc and the carrier signal 1 is 0.75 vpp while the carrier signal 2 is 0.25 Vdc and the negative of the carrier signal 4 is -25 Vdc, the vpp of the composition modulation signal is equal to VDC. Figure 5 shows the implementation of the proposed scheme is similar to the conventional CBPWM modulation technique, the difference is that the carrier signal shifted two up the positive axis and two down the negative axis. The signal amplitudes have the same magnitude, i.e. each other has the opposite value.
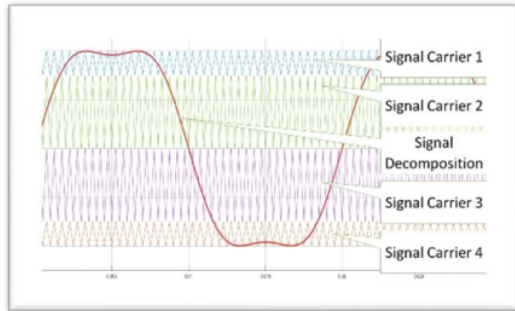
Figure 5 Composition of new carrier wave

TABLE I. PARAMETER DIODE AND IGBT

DIODE	IGBT
C1=C2=4.7e-3 F	Ron= 0.001 Ω
R1=R2 0.01Ω	Rs =1e5 Ω
R Snubber = 250e-9F	Cs = inf
Ron=0.001 Ω	Speed Time=5e6 s
R Snubber =500 Ω	Vdc=100 Volt -
Vf= 0.8 Volt	

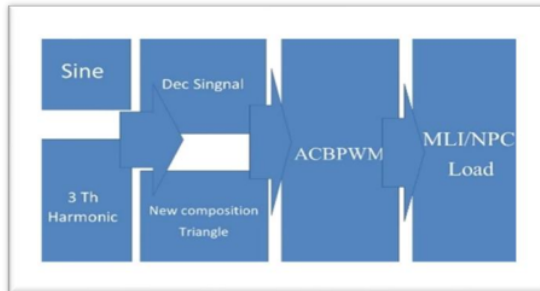*B. Block Diagram of Advanced Carrier Based Pulse Width Modulation*

Figure 6. Block Diagram of ACBPWM

The first carrier wave with a value of 0.75 Vdc, the second carrier wave of 0.25 Vdc which is lowered to the level above, as well as for the negative side. The basic concept proposed this time is to use a Neutral Point Clamped multilevel inverter load. It looks like Figure 6. The parameters of the NPC inverter are shown in Table 1.
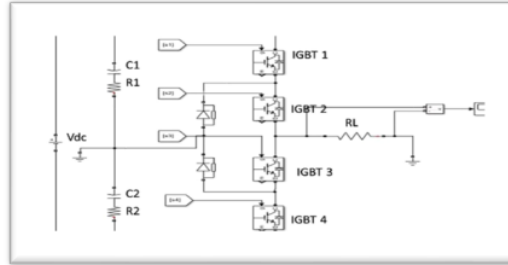

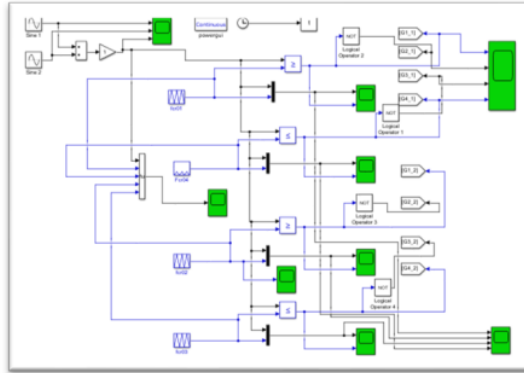Figure 7. The Scheme of NPC With Resistive Load

Figure 8 Scheme Detail for ACBPWM.

The block diagram of the ACBPWM control scheme is shown in Figure 10. The sine signal is injected with the third harmonic signal to produce a decomposition signal. The decomposition signal is compared with the new triangular pattern signal (New Composition) into ACBPWM which is then used to control the Multilevel Inverter, in this case the NPC. Figure 7 shows a single-phase NPC scheme using IBGT. The ACBPWM technique is shown in Figure 8 which is equipped with a measuring instrument using MATLAB Simulink software.

V. RESULT AND DISCUSSION

The results of the simulation of the proposed scheme are shown in Figures 8 to 12. Figure 9. shows the conventional CBPWM modulation technique. In this method the carrier signal has the same amplitude in the positive and negative cycles. The complete increase in the complete modulation index is shown in Table 2. Figures 9 to 11 show a decrease in THD. This decrease gives an indication that this technique can improve the power quality of the inverter. The details can be shown in Table 2.

By setting the modulation index from 0.1 to 1.15, the decrease in THD is very noticeable as well as the increase in power quality
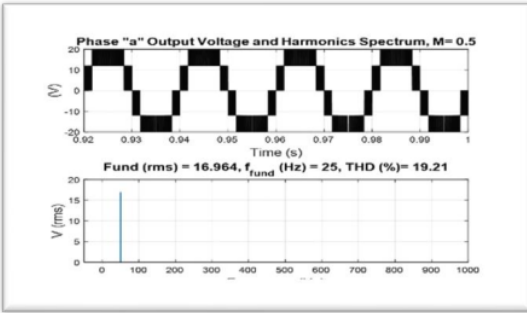
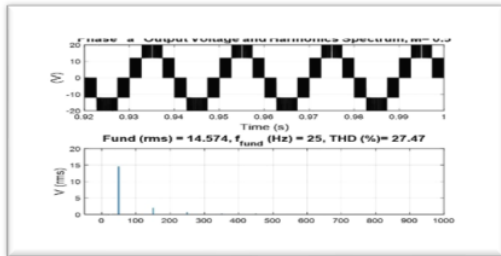Figure 9. Voltage Output from Decomp CBPWM.
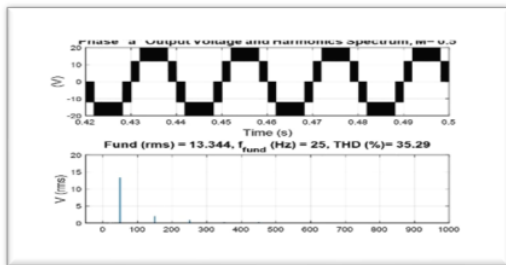

Figure 10. Signal Output from Advance CBPWM.

Figure 11. Signal Output from CBPWM.

VI. CONCLUTION

The control scheme of the Advanced CBPWM technique Multilevel Inverter which is applied by adjusting the position of the carrier signal in the form of a triangle signal and modifying the signal on the modulating signal with the third harmonic signal decomposition technique. The proposed scheme improves power quality, by using a fixed dc source. Changes in the mean THD of 19,21%, 27,5% and 35,28%. For Advance CBPWM, decomposition CBPWM, Conventional CBPWM respectively.

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